Abstracts

A Table Based Bias and Temperature Dependent Small Signal and Noise Equivalent Circuit Model

A new algorithm is presented for construction of an accurate table-based bias and temperature dependent FET small-signal and noise model. The algorithm provides orders of magnitude data reduction over the alternate approach of storing multiple S-parameter and noise parameter data files (to represent different bias and temperature conditions). The algorithm performs 2-D linear interpolation on a single stored data table to quickly produce accurate bias and temperature dependent model simulations.

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